Figure 7.1  The logical machine model of a thread-based programming paradigm.
Figure 7.2 Execution time of the `compute_pi` program as a function of number of threads.
Figure 7.3 Execution time of 1000 sequential and logarithmic barriers as a function of number of threads on a 32 processor SGI Origin 2000.
Figure 7.4 A sample OpenMP program along with its Pthreads translation that might be performed by an OpenMP compiler.
Figure 7.5  Three different schedules using the static scheduling class of OpenMP.